

In the Claims:

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1. (Currently Amended) A system for de-interleaving data in a wireless receiver comprising  
a memory buffer to store the data; and  
means, coupled to said memory buffer, for performing a first and second de-interleaving of the data stored in said memory buffer, wherein said means includes means for reading and writing the data to the memory buffer in connection with said first and second de-interleaving.
  2. (Original) The system of claim 1, wherein said means performs said second de-interleaving as the data is written to said memory buffer and performs said first de-interleaving as stored data is read from said memory buffer.
  3. (Original) The system of claim 1, wherein said memory buffer stores the data, and wherein said means performs said first and second de-interleaving as the stored data is read from said memory buffer.
  4. (Original) The system of claim 2, wherein the data comprises radio frames, said memory buffer comprises a plurality of radio frame blocks, and said means causes said radio frames to be stored in said radio frame blocks.
  5. (Original) The system of claim 4, wherein the data is transmitted over one or more physical channels, wherein each of said radio frames comprises a physical channel frame associated with each physical channel, each of said radio frame blocks comprises a physical channel block associated with each physical channel, and said means causes said physical channel frames to be stored in said physical channel blocks.

6. (Currently Amended) A receiver that receives data via a wireless link, said receiver comprising:

a demodulator coupled to the wireless link;

a decoding/demultiplexing unit, coupled to said demodulator, that includes:

a memory buffer to store the data, and

means, coupled to said memory buffer, for performing a first and second de-interleaving, wherein said means includes means for reading and writing the data to the memory buffer in connection with said first and second de-interleaving; and

a medium access control layer coupled to said decoding/demultiplexing unit.

7. (Original) The receiver of claim 6, wherein said memory buffer comprises a plurality of radio frame blocks.

8. (Original) The receiver of claim 7, wherein each of said radio frame blocks comprises a physical channel block.

9. (Original) A system for de-interleaving data in a wireless receiver comprising:  
a memory buffer; and  
a read/write unit, coupled to said memory buffer, wherein said read/write unit is configured to perform a first and second de-interleaving of the data.

10. (Original) The system of claim 9, wherein said read/write unit performs said second de-interleaving as the data is written to said memory buffer and performs said first de-interleaving as stored data is read from said memory buffer.

11. (Original) A method for de-interleaving data in a wireless receiver comprising:  
performing a second de-interleaving as the data is written to a memory buffer;  
and  
performing a first de-interleaving as data is read from said memory buffer.

12. (Original) The method of claim 11 further comprising:  
reassembling one or more physical channels from the data stored in said memory buffer;  
performing a second removal of discontinuous transmission indication bits from the data stored in said memory buffer;  
demultiplexing the data stored in said memory buffer into a plurality of transport channels; and  
reassembling transport blocks from the data stored in said memory buffer, wherein the data comprises radio frames.
13. (Original) A method comprising:  
demodulating data received via a wireless link;  
writing said data to a memory buffer according to a second de-interleaving pattern; and  
reading said data from said memory buffer according to a first de-interleaving pattern, forming an output data stream; and  
decoding said output data stream.

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14. (New) The method of claim 13 further comprising:  
reassembling one or more physical channels from the data stored in said memory  
buffer;  
performing a second removal of discontinuous transmission indication bits from  
the data stored in said memory buffer;  
demultiplexing the data stored in said memory buffer into a plurality of transport  
channels; and  
reassembling transport blocks from the data stored in said memory buffer,  
wherein the data comprises radio frames.

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